

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In the Matter of the Application of:

Keith M. Murr et al.

Serial No.: 10/056,615

Filed: January 23, 2002

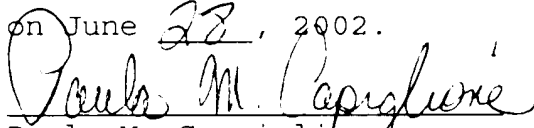
For: Cover For Electronic Components And Method Of Using Same
During Component Assembly

Examiner: Unknown Group Art Unit: Unknown

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Assistant Commissioner for Patents

Washington, DC 20231

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, DC 20231 on June 28, 2002.

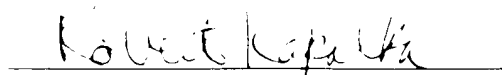

Paula M. CapriglioneCOPY OF PAPERS
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TECHNOLOGY CENTER 2800**INFORMATION DISCLOSURE
STATEMENT TRANSMITTAL LETTER**

Sir:

An Information Disclosure Statement is submitted herewith pursuant to 37 CFR §§1.97-1.98. The enclosed statement is being filed within three months of the filing date of a national application, or within three months of the date of entry into the national stage as set forth in 37 CFR §1.491 in an international application, or before the mailing date of a first Office Action on the merits, whichever event occurs last. However, in the event a fee is due in connection with this communication, the Commissioner is authorized to charge Deposit Account No. 23-1950.

Respectfully submitted,

Applicant's:


Robert J. Kapalka

Assistant Commissioner for Patents

Inventor: 302 633-2776

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